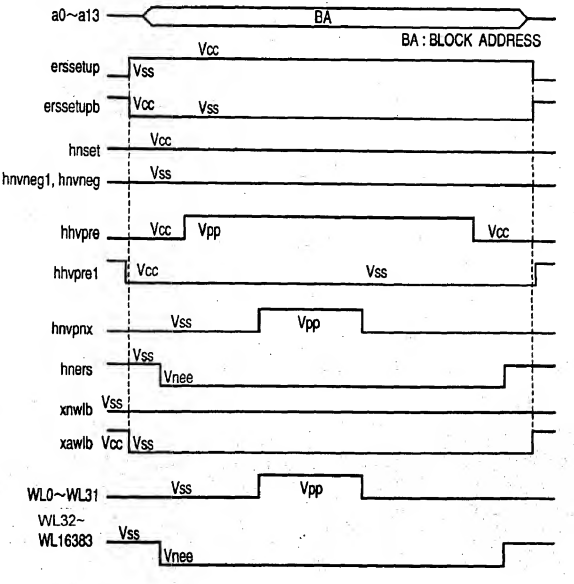
REPLACEMENT SHEET

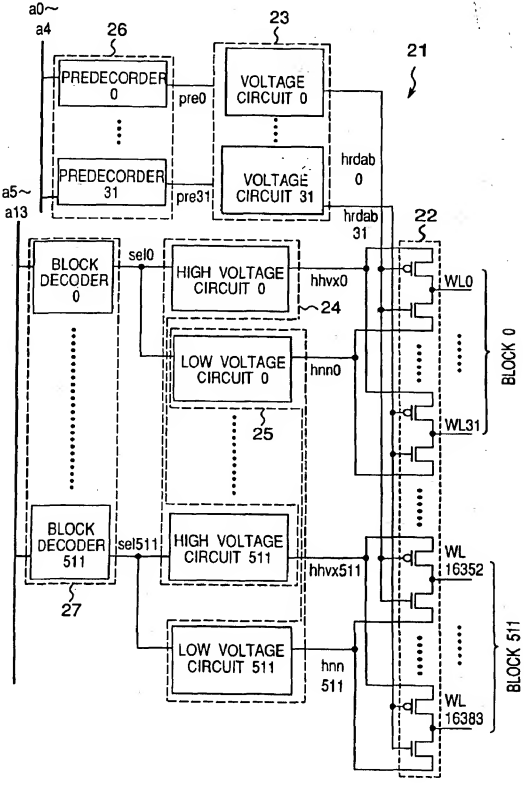
Fig.11A
Fig.11B
Fig.11C
Fig.11D
Fig.11E
Fig.11F
Fig.11G
Fig.11H
Fig.11I
Fig.11J
Fig.11K
Fig.11L
Fig.11M



Approved
10/7/03

App No.: 09/598,384 Docket N .: 204552018400
Inventor: Yasuaki HIRANO
Title: ERASE METHOD FOR NONVOLATILE SEMICONDUCTOR STORAGE DEVICE AND ROW DECODER CIRCUIT FOR FULFILLING THE METHOD

Fig.12 **REPLACEMENT SHEET**



REPLACEMENT SHEET

Fig.13

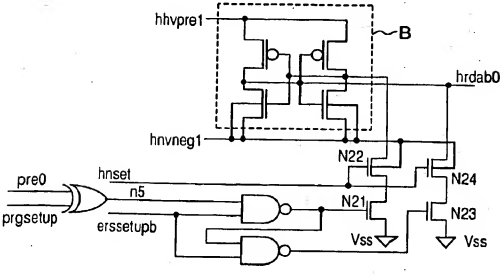


Fig.14

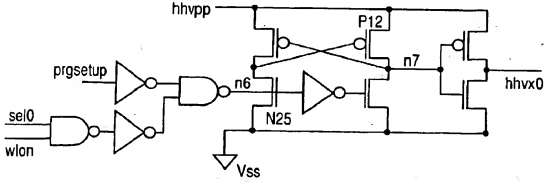


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Fig.19 PRIOR ART

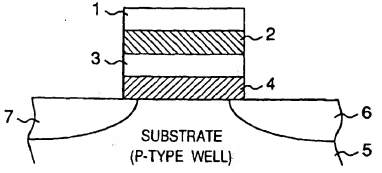
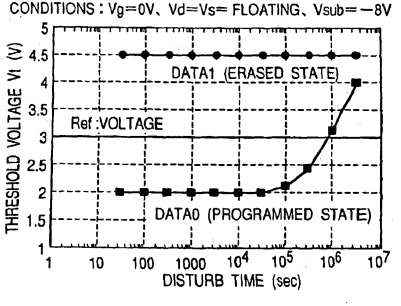


Fig.20 PRIOR ART



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REPLACEMENT SHEET

Fig.21 PRIOR ART